What is the proper jumper setting of Jumper JP2?

This is for the mode of the pcb of how the board is programmed. The top, qspi, programs the board automatically from memory on the board. The middle is for jtag and the bottom for usb.

What is the proper jumper setting of Jumper JP1?

The left jumper settings determine how the pcb receives power. The bottom two pins are for usb power and the top two are for some external source.

What do you see on the seven-segment displays?

The display cycles through the various digits it can display from 0 to 9.

Describe, for each one, what happens when you push the various buttons (BTNC, BTNL, BTNR, BTNU, BTND)?

BTNC - disable and reset full display

BTNL - diable 3rd digit display

BTNR - diable 2nd digit display

BTNU - diable 4th digit display

BTND - diable 1st digit display

| **SW3** | **SW2** | **SW1** | **SW0** | **Digit** |
| --- | --- | --- | --- | --- |
| 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 0 | 1 | 1 |
| 0 | 0 | 1 | 0 | 2 |
| 0 | 0 | 1 | 1 | 3 |
| 0 | 1 | 0 | 0 | 4 |
| 0 | 1 | 0 | 1 | 5 |
| 0 | 1 | 1 | 0 | 6 |
| 0 | 1 | 1 | 1 | 7 |
| 1 | 0 | 0 | 0 | 8 |
| 1 | 0 | 0 | 1 | 9 |
| 1 | 0 | 1 | 0 | A |
| 1 | 0 | 1 | 1 | b |
| 1 | 1 | 0 | 0 | C |
| 1 | 1 | 0 | 1 | d |
| 1 | 1 | 1 | 0 | E |
| 1 | 1 | 1 | 1 | F |

| **SW6** | **SW5** | **SW4** | **LD4** | **LD3** | **LD2** | **LD1** | **LD0** |
| --- | --- | --- | --- | --- | --- | --- | --- |
| 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |
| 0 | 0 | 1 | 1 | 0 | 1 | 1 | 0 |
| 0 | 1 | 0 | 1 | 0 | 1 | 1 | 0 |
| 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 |
| 1 | 0 | 0 | 1 | 0 | 1 | 1 | 0 |
| 1 | 0 | 1 | 1 | 0 | 0 | 1 | 0 |
| 1 | 1 | 0 | 1 | 0 | 0 | 1 | 0 |
| 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 |